TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

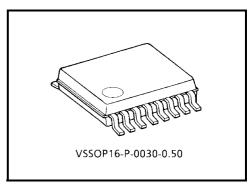
TC7MB3257FK

Quad 1-of-2 Multiplexer/Demultiplexer

The TC7MB3257FK is high-speed CMOS quad 1-2 multiplexer/demultiplexer. The low on resistance of the switch allows connections to be made with minimal propagation delay time.

This device consists of four individual two-inputs multiplexer/demultiplexer with common select input (S) and output enable (\overline{OE}) . The A inputs is connected to the B1 or B2 outputs determined by the combination both the select input (S) and output enable (\overline{OE}) . When the output enable (\overline{OE}) input is held "H" level, the switches are open with regardless the state of select inputs and a high-impedance state exists between the switches.

All inputs are equipped with protection circuits against static discharge.

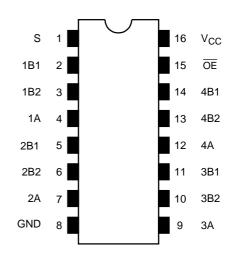


Weight: 0.02 g (typ.)

Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed: $t_{pd} = 0.25 \text{ ns (max)}$
- Low on resistance: $RON = 5 \Omega$ (typ.)
- ESD performance: Machine model $> \pm 200 \text{ V}$ Human body model $> \pm 2000 \text{ V}$
- Compatible with TTL outputs (control inputs)
- Package: VSSOP (US16)
- Pin compatible with the 74xx257 type.
 Functionally equivalent to (FST/CBT) 3257.

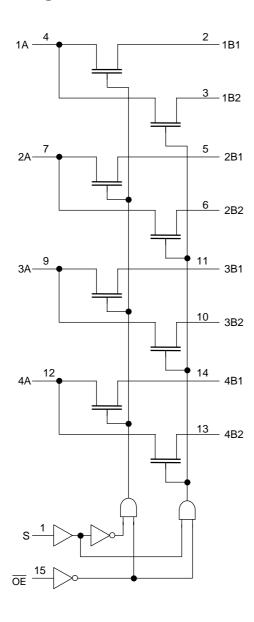
Pin Assignment (top view)



Truth Table

Inp	outs	Function
ŌE	S	Tunction
L	L	A port = B1 port
L	Н	A port = B2 port
Н	Х	Disconnect

System Diagram



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Maximum Ratings

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Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC switch voltage	Vs	-0.5~7.0	V
Input diode current	I _{IK}	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	P _D	180	mW
DC V _{CC} /GND current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	Vs	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Characte	rietice	Symbol	Test Condition			Min	Тур.	Max	Unit
Griaracie	1131103	Cymbol	rest oond	uon	V _{CC} (V)	IVIIII	(Note1)	IVIAX	Offic
Input voltage	"H" level	V_{IH}	_		4.5~5.5	2.0	_	_	V
Input voltage	"L" level	V _{IL}	_		4.5~5.5	_	_	0.8	V
Input leakage cur	rent (OE , S)	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	_	±1.0	μΑ
Power off leakage	e current	l _{OFF}	A, B, $\overline{OE} = 0 \sim 5.5 \text{ V}$		0	_	_	±1.0	μΑ
Off-state leakage	current	I _{SZ}	A, B = 0~5.5 V, $\overline{OE} = V_{CC}$		4.5~5.5			±1.0	μА
(switch off)		182			4.5~5.5			±1.0	μΑ
ON resistance			V _{IS} = 0 V	$I_{IS} = 64 \text{ mA}$	4.5		5	7	
ON resistance	(Note2)	R _{ON}	V 1S - 0 V	$I_{IS} = 30 \text{ mA}$	4.5		5	7	Ω
	(Note2)		$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$		4.5		10	15	
Increase in I _{CC} p	er innut	I _{CC}	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$		5.5		_	10	μΑ
mercase in icc p	ci iliput	Δl _{CC}	V _{IN} = 3.4 V (one input)		5.5	_	_	2.5	mA

Note 1: Typical values are at $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}C$.

Note 2: Measured by the voltage drop between A and B pins at the indicated current through the switch.

ON resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2 (Note3)	4.5		0.25	no
(bus to bus)	t _{pHL}	Figure 1, Figure 2 (Note3)	4.5	_	0.23	ns
Propagation delay time	t _{pLH}	Figure 4 Figure 2	4.5		5 0	
(S to bus)	t _{pHL}	Figure 1, Figure 2	4.5	_	5.0	ns
Output enable time	t _{pZL}	Figure 4 Figure 0	4.5			
(OE to bus)	t _{pZH}	Figure 1, Figure 3	4.5	_	5.5	ns
Output enable time	t _{pZL}	F: 4 F: 0	4.5			
(S to bus)	t _{pZH}	Figure 1, Figure 3	4.5	_	5.5	ns
Output disable time	t _{pLZ}	Figure 4 Figure 2	4.5		5 0	
(OE to bus)	t _{pHZ}	Figure 1, Figure 3	4.5	_	5.0	ns
Output disable time	t _{pLZ}	Firm 4 Firm 0	4.5		5.0	
(S to bus)	t _{pHZ}	Figure 1, Figure 3	4.5	_	5.0	ns

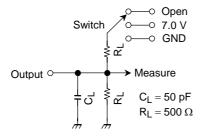
Note 3: This parameter is guaranteed by design but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical on resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage the source (zero output impedance).

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance ($\overline{\text{OE}}$, S)	C _{IN}	(Note4)	5.0	3	pF
Switch terminal capacitance (B1~2)	C _{I/O}	$\overline{OE} = V_{CC}$ (Note4)	5.0	10	pF
Switch terminal capacitance (A)	C _{I/O}	$\overline{OE} = V_{CC}$ (Note4)	5.0	17	pF

Note 4: This parameter is guaranteed by design.

AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
t _{pLZ} , t _{pZL}	7.0 V		
t _{pHZ} , t _{pZH}	Open		

Figure 1

AC Waveform

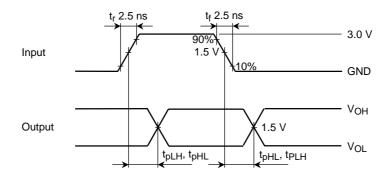


Figure 2 t_{pLH}, t_{pHL}

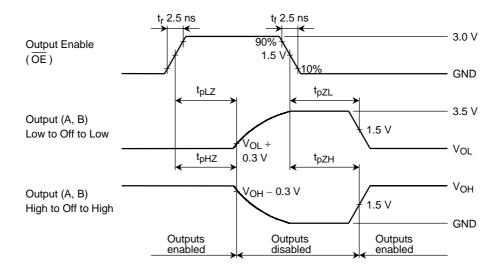
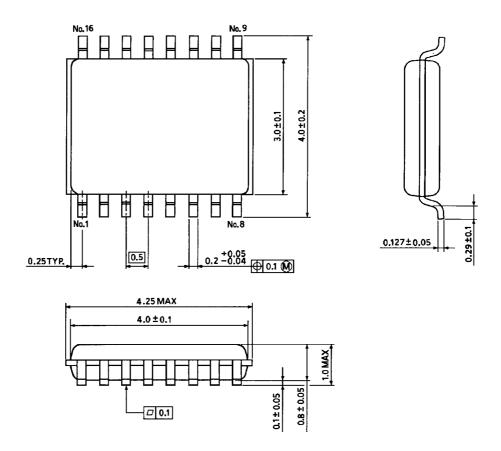


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

Package Dimensions

VSSOP16-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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